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Sheet 1 of 1

Form PTO-1449 <b>LIST OF ART CITED BY APPLICANT</b> <small>(Use several sheets if necessary.)</small>				U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 50983-1764	SERIAL NO. 09/677,375	
				APPLICANT Block et al.			
				FILING DATE October 2, 2000	GROUP 1765		
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
LV	AA	6,533,053 B1	03/18/03	Allen, III			
	AB	5,366,333	02/09/99	Chen et al.			
	AC	5,365,938	02/09/99	Pecora et al.			
	AD	5,343,841	12/01/98	Pu et al.			
	AE	5,076,666	06/23/91	Hills et al.			
LV	AF	6,184,322 B1	05/13/02	Grossi et al.			
	AG						
	AH						
	AI						
	AJ						
	AK						
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation
	AL						Yes
	AM						No
	AN						
	AO						
	AP						
<b>OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)</b>							
LV	AR			Stanley Wolf and Richard Tauber. Silicon Processing for the VLSI Era, Vol. 1, pp 549. 1986.			
	AS						
	AT						
EXAMINER <b>LAN VINH</b>				DATE CONSIDERED <b>3/4/2004</b>			
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with 37 CFR 1.56; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							